

Each of Figs. 7 (a) - 7 (d) is an illustration showing how a form is specified in an embodiment of the invention.

Fig. 8 is an illustration showing how a form is specified in an example of an embodiment of the invention.

Fig. 9 is a functional block diagram showing the structure of an image processing apparatus and a flow (processing flow) of image processing in an example of an embodiment for edge feature amount extracting in the invention.

/ Each of Figs. 10 (a) - 10 ^(d)[(c)] is an illustration showing a form of a filter for obtaining a signal variance between neighboring pixels in an example of an embodiment of the invention.

Each of Figs. 11 (a) - 11 (c) is an illustration showing the state of edge distribution in an example of an embodiment of the invention.

Each of Figs. 12 (a) and 12 (b) is an illustration showing the state of edge distribution in an example of an embodiment of the invention.

Fig. 13 is a functional block diagram showing the structure of an image processing apparatus and a flow (processing flow) of image processing in an example of an embodiment for feature amount right and wrong judgment in the invention.

09840193-082301
T08280-EST04850